A Report on Industry Interaction with "Maven Silicon, Bengaluru Regarding Internship Opportunities and Student Placement"

Organised by Department of Electronics and Communication Engineering on 23.09.2026



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Introduction:

The Department of Electronics and Communication Engineering (ECE), Madanapalle Institute of Technology & Science (MITS), Madanapalle, Andhra Pradesh, undertook an industry interaction with Mr. Vijay Kumar Kannur and other officials of Maven Silicon, Bengaluru, with the objective of strengthening the connection between academic learning and the semiconductor/VLSI industry. The interaction was led by Prof. Sanjay Kumar C. Gowre, Head of the Department, ECE, along with Dr. Simadri Badatya, Assistant Professor, ECE, who interacted with Maven Silicon officials.

The discussion focused on exploring avenues for student internships and placement support, understanding industry expectations, and identifying the technical competencies that students should develop to improve their readiness for professional roles. The interaction was intended to encourage closer collaboration between the institution and industry and to help students connect classroom concepts with practical engineering requirements.

Purpose of the Interaction:

The primary purpose was to establish a meaningful dialogue with Maven Silicon regarding internship opportunities and possible placement-related engagement for ECE students. The meeting also provided an opportunity to discuss the relevance of specialised technical training, industry-oriented learning, and skill development for students aspiring to work in VLSI design and related semiconductor domains.

Objectives

- To explore internship opportunities for ECE students through industry engagement with Maven Silicon.
- To discuss possible placement-related support and pathways for students who meet the relevant skill requirements.
- To understand industry expectations concerning technical knowledge, practical skills, communication, and professional readiness.
- To identify the importance of specialised training in VLSI design and semiconductor-related areas.
- To strengthen collaboration between MITS and industry for student training, career guidance, and employability development.
- To encourage students to take up skill-development activities, hands-on practice, and project work aligned with industry needs.

Interaction with Maven Silicon Officials

During the interaction, Prof. Sanjay Kumar C. Gowre, Head of the Department of ECE, and Dr. Simadri Badatya, Assistant Professor, ECE, engaged with Maven Silicon officials on the possibilities of supporting students through internship and placement-oriented initiatives. The faculty representatives highlighted the department's interest in providing students with opportunities to gain practical exposure and to prepare for the expectations of the semiconductor and VLSI industry.

The interaction provided a forum to discuss how academic preparation can be complemented by industry-relevant training. Particular emphasis was placed on the value of strengthening fundamental electronics knowledge, developing design and problem-solving skills, undertaking hands-on technical exercises, and preparing for technical assessments and interviews. These areas are important for students seeking opportunities in VLSI design and allied engineering roles.

The faculty members also discussed the importance of early career planning and encouraging students to build a strong technical profile through coursework, laboratory practice, mini-projects, and continuous learning. Engagement with industry representatives can help students gain a clearer understanding of professional expectations and the skills they need to develop before applying for internships or employment.

Relevance to ECE Students:

Maven Silicon's industry focus makes the interaction relevant to students interested in VLSI and semiconductor-related careers. For ECE students, internship exposure and structured technical preparation can help bridge the gap between theoretical concepts and engineering practice. The department can use the insights from such interactions to guide students towards relevant learning activities, including digital electronics, CMOS fundamentals, HDL-based design, verification concepts, and project-based learning, according to the requirements of the opportunities available.
Students are encouraged to maintain a consistent record of technical learning, participate in laboratory and project work, strengthen aptitude and communication skills, and prepare updated resumes that clearly present their technical competencies. Any internship or placement opportunity will remain subject to the organization's eligibility criteria, selection process, and availability.

Expected Outcomes and Follow-up

Area	Expected outcome
Internship awareness	Students can be guided towards relevant internship opportunities and the preparation needed to apply.
Placement readiness	The department can encourage structured preparation for technical interviews, assessments, and recruitment processes.
Industry-aligned learning	Coursework, laboratory practice, and projects can be connected more closely with current industry expectations.
Institution–industry engagement	The interaction can serve as a foundation for continued communication and future collaborative activities.
Student career guidance	Faculty can advise students on technical skill development and professional preparation for VLSI-related roles.

Suggested Follow-up Actions

- Share relevant internship and placement information with eligible students when details are confirmed by the organisation.
- Identify interested students and assess their readiness based on the criteria communicated by Maven Silicon.
- Encourage students to strengthen core ECE/VLSI concepts, practical design skills, aptitude, communication, and interview preparation.
- Maintain communication with Maven Silicon officials regarding future internship drives, training programmes, and placement-related activities.
- Document confirmed opportunities, eligibility conditions, timelines, and student outcomes for departmental records.

Acknowledgment:

The Department of Electronics and Communication Engineering, MITS, expresses its appreciation to the institutional leadership for supporting industry engagement initiatives and to the Maven Silicon officials for interacting with the faculty representatives. Such interactions are valuable in strengthening academic–industry relationships and supporting students' career development.